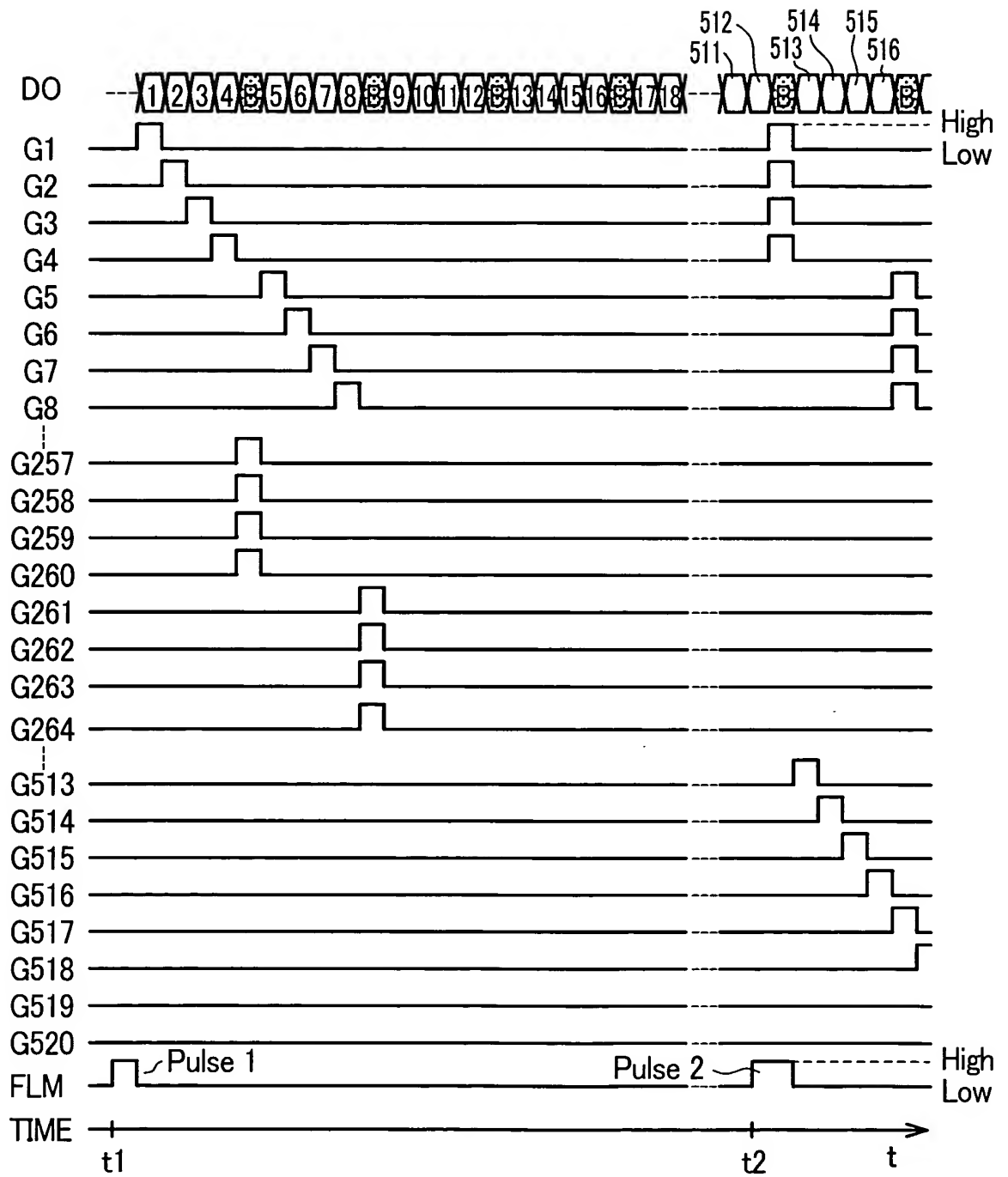


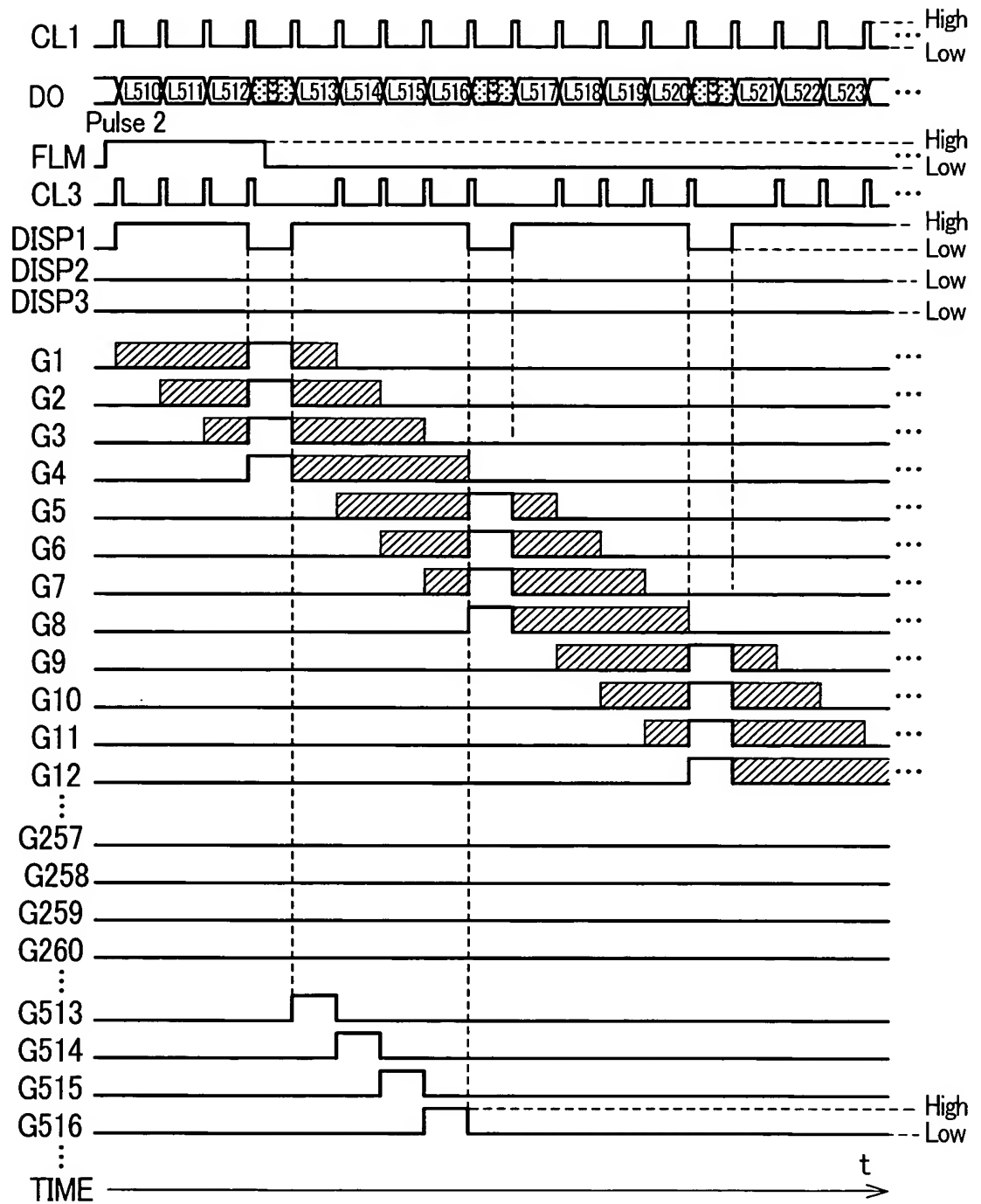
*FIG. 1*



The diagram illustrates the timing of various signals relative to a horizontal period. The horizontal axis is labeled 'TIME' and 't'. A long double-headed arrow at the top is labeled 'FLT'. The signals are as follows:

- VSYNC**: A single pulse at the start of the horizontal period, with levels 'High' and 'Low' indicated.
- HSYNC**: A series of pulses, with levels 'High' and 'Low' indicated.
- Input Data**: A series of data blocks labeled L1, L2, L3, L4, L5, etc. Each block is preceded by a 'TR' (turn-around) time. The horizontal period is divided into 'HPD' (Horizontal Period) and 'DLY' (Delay) sections.
- FLM1**: A signal with 'Pulse 1' at the start of the horizontal period, with levels 'High' and 'Low' indicated.
- CL1**: A series of pulses, with levels 'High' and 'Low' indicated.
- Driver Data**: A series of data blocks labeled L1, L2, L3, L4, etc. Each block is preceded by a 'TR' (turn-around) time.

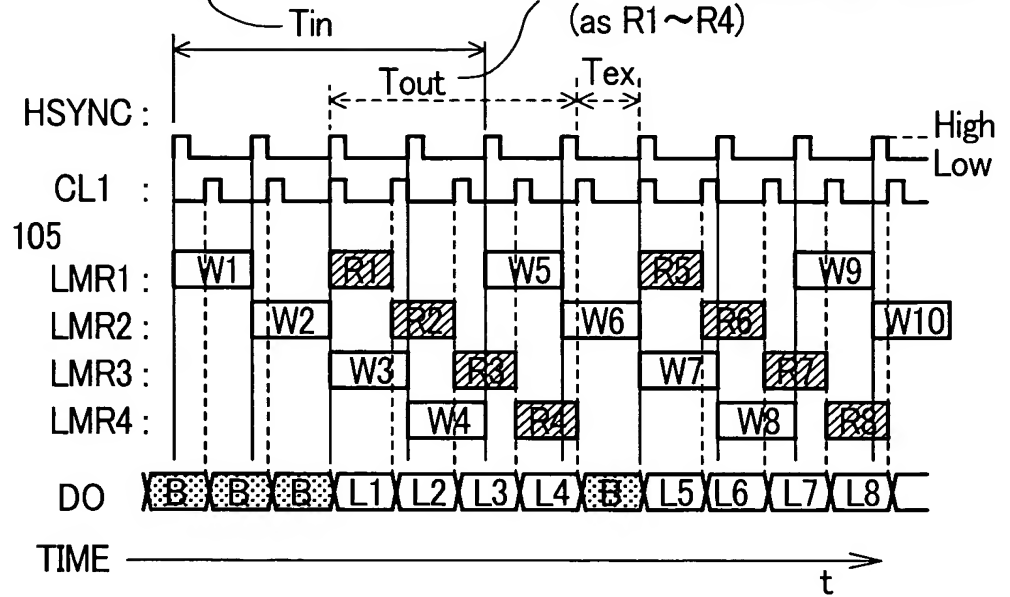
FIG. 4



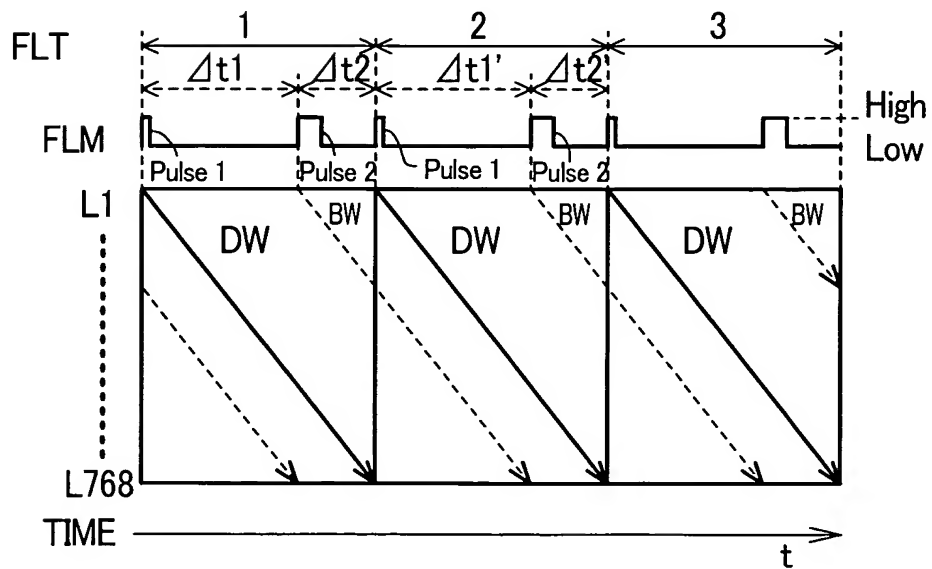
# FIG. 5

Acquisition Period for  
4 Lines of an Image Datum (as W1 ~ W4)

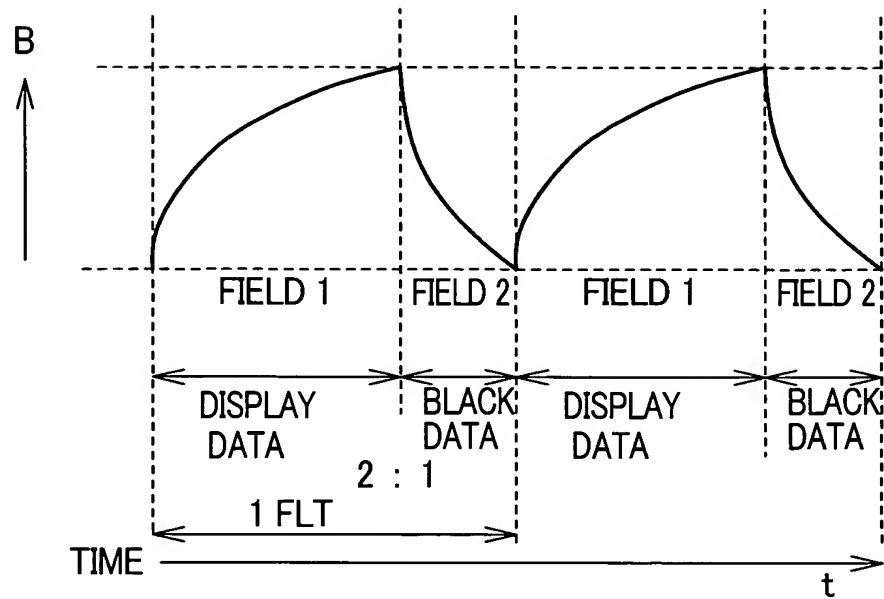
Transmitting Period for  
the 4 Lines of the Image Datum  
(as R1 ~ R4)



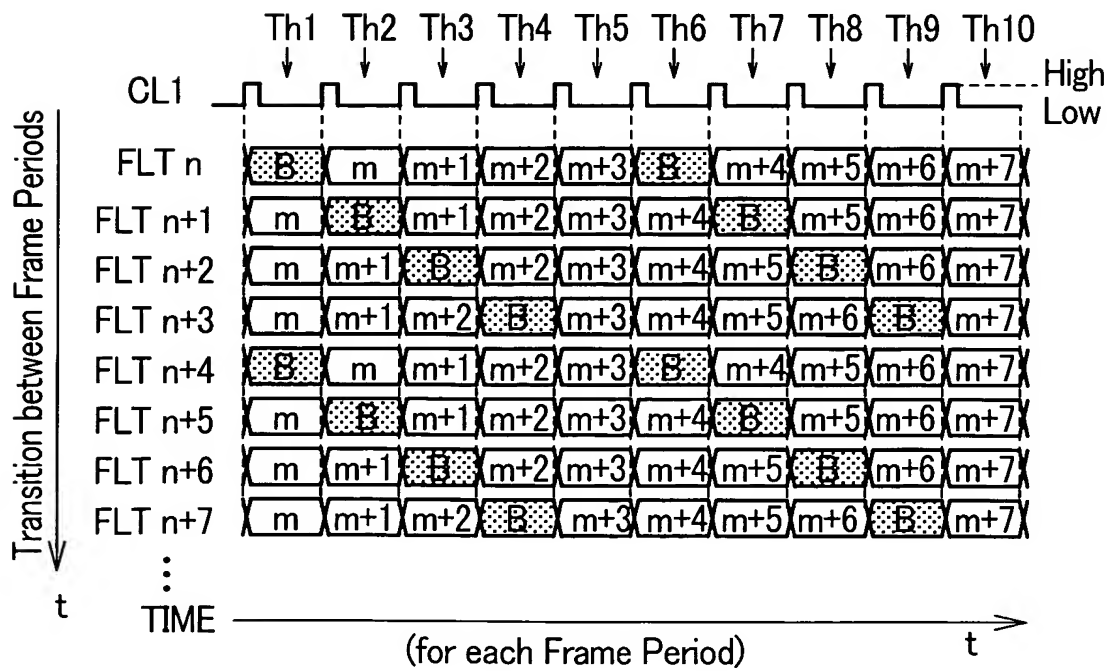
# FIG. 6



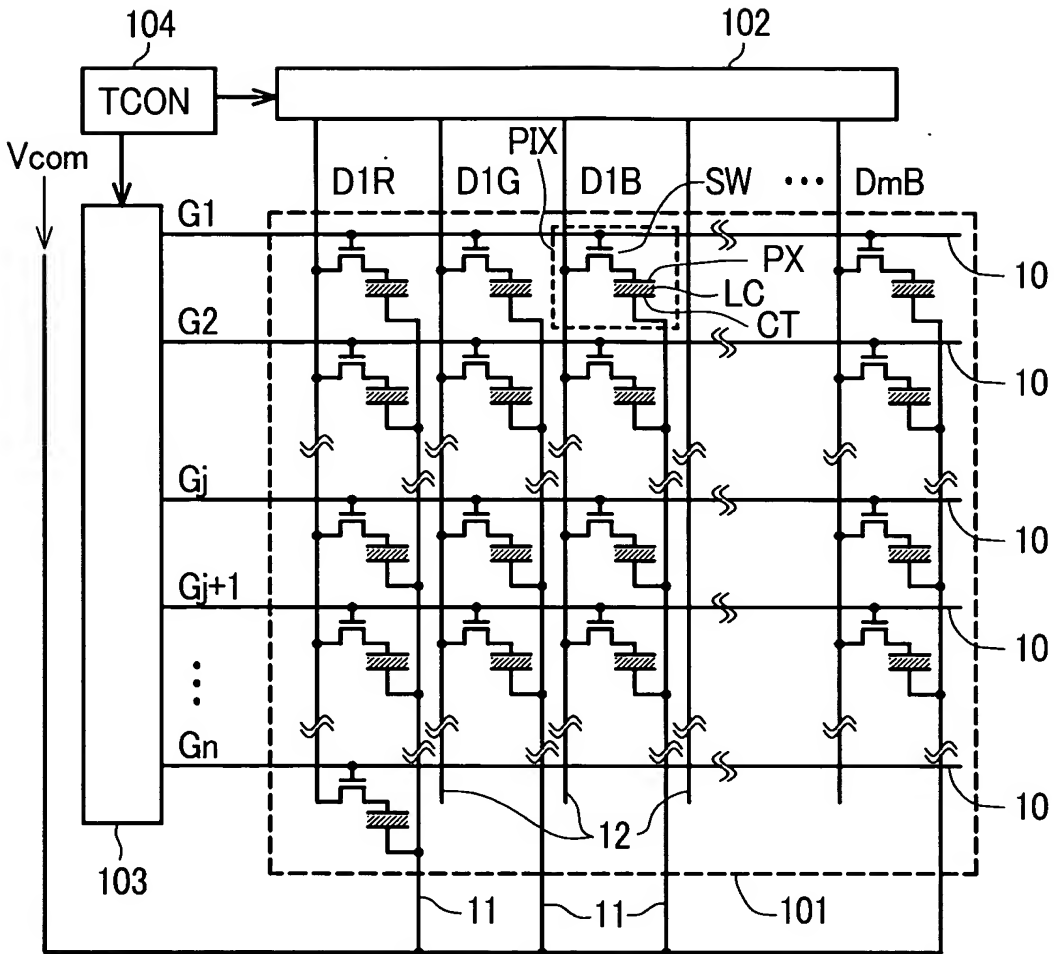
*FIG. 7*



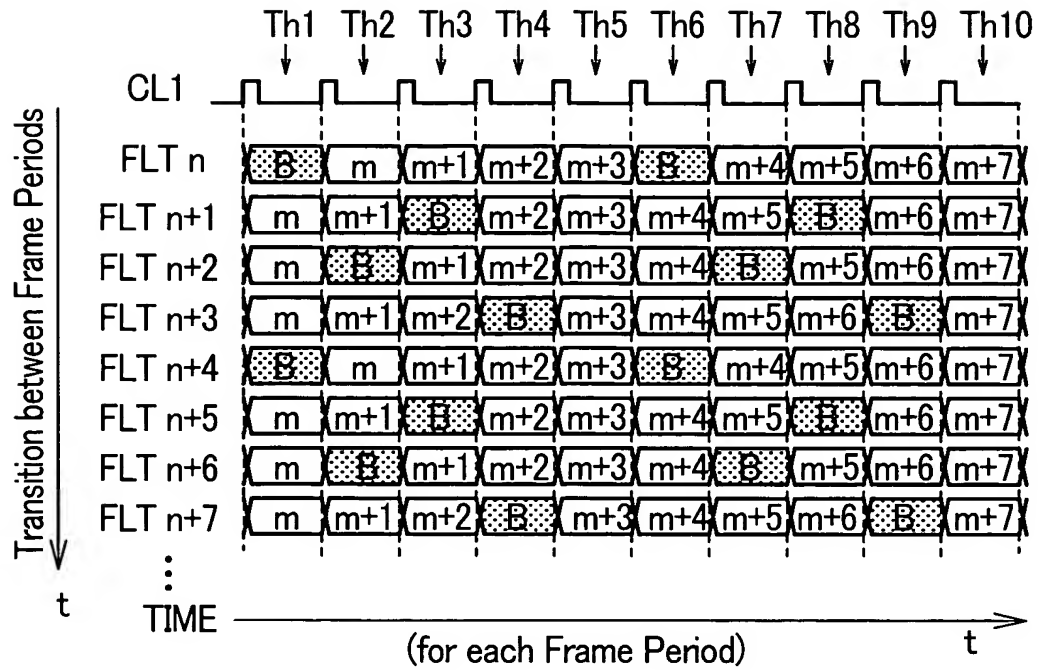
*FIG. 8*



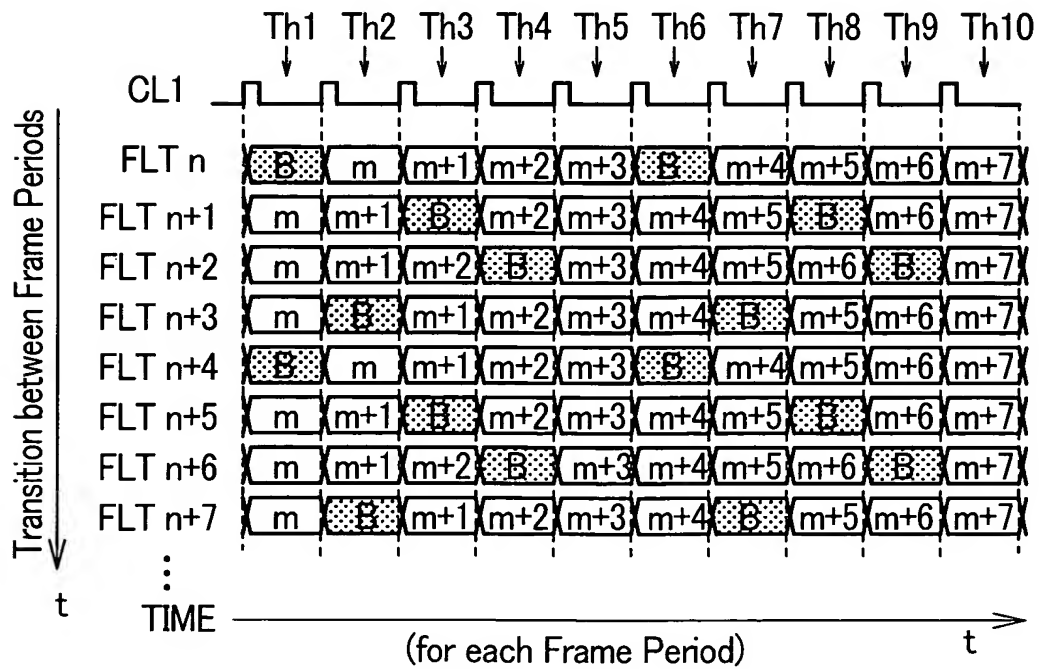
*FIG. 9*



# FIG 10

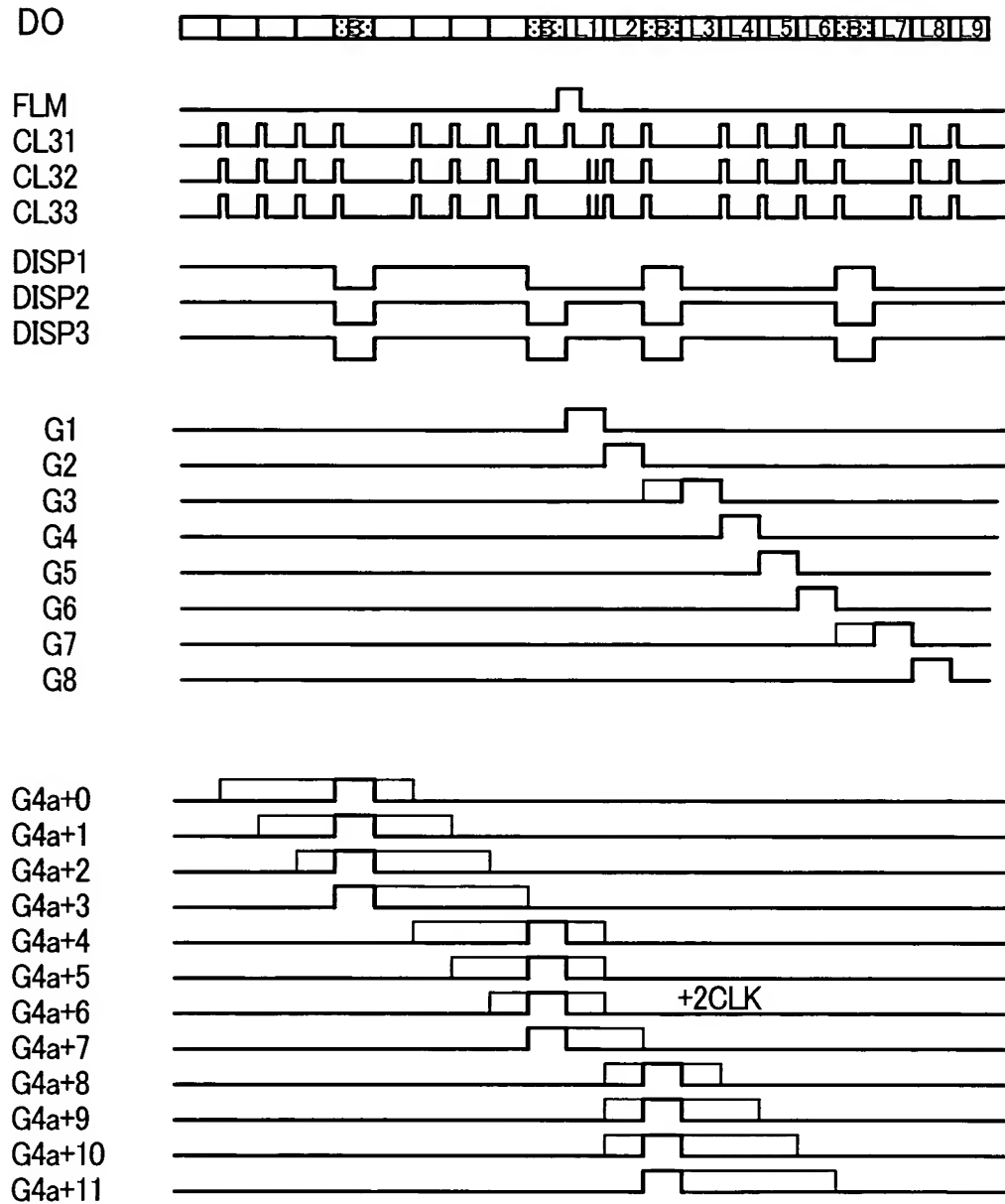


# FIG. 11



# FIG. 12

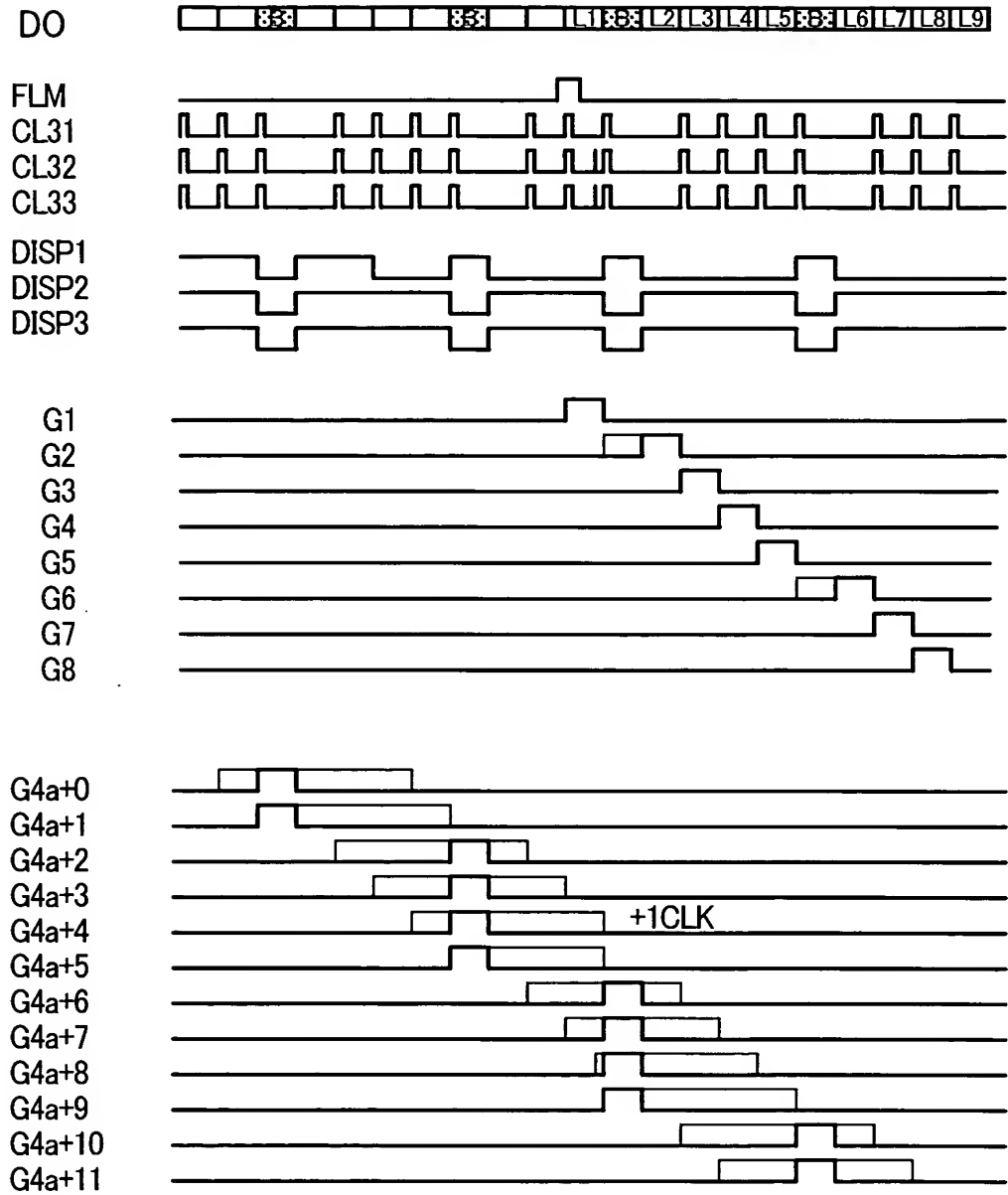
4n+0 F1 ① → F2 ③





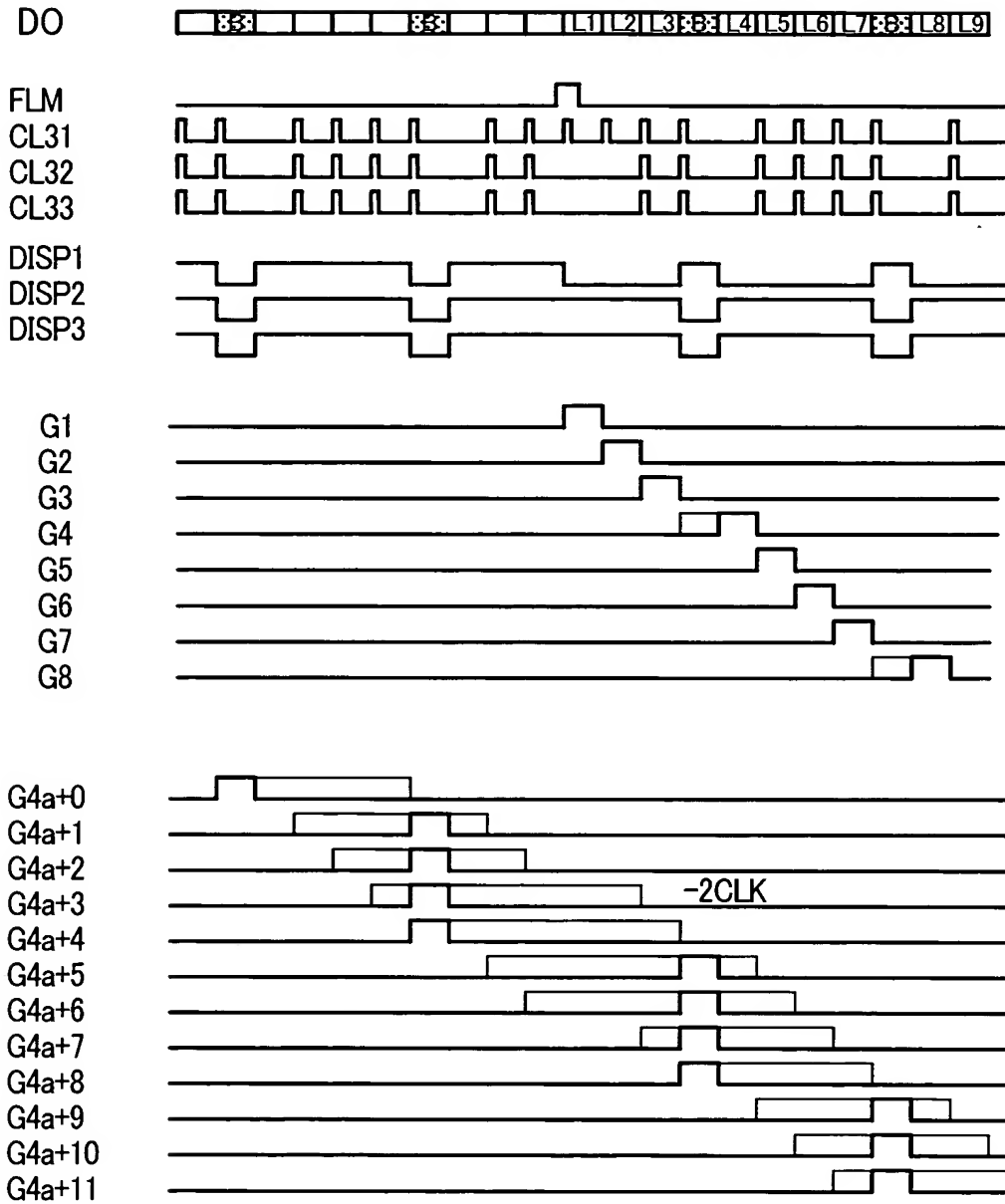
# FIG. 13

4n+0 F2 ③ → F3 ②



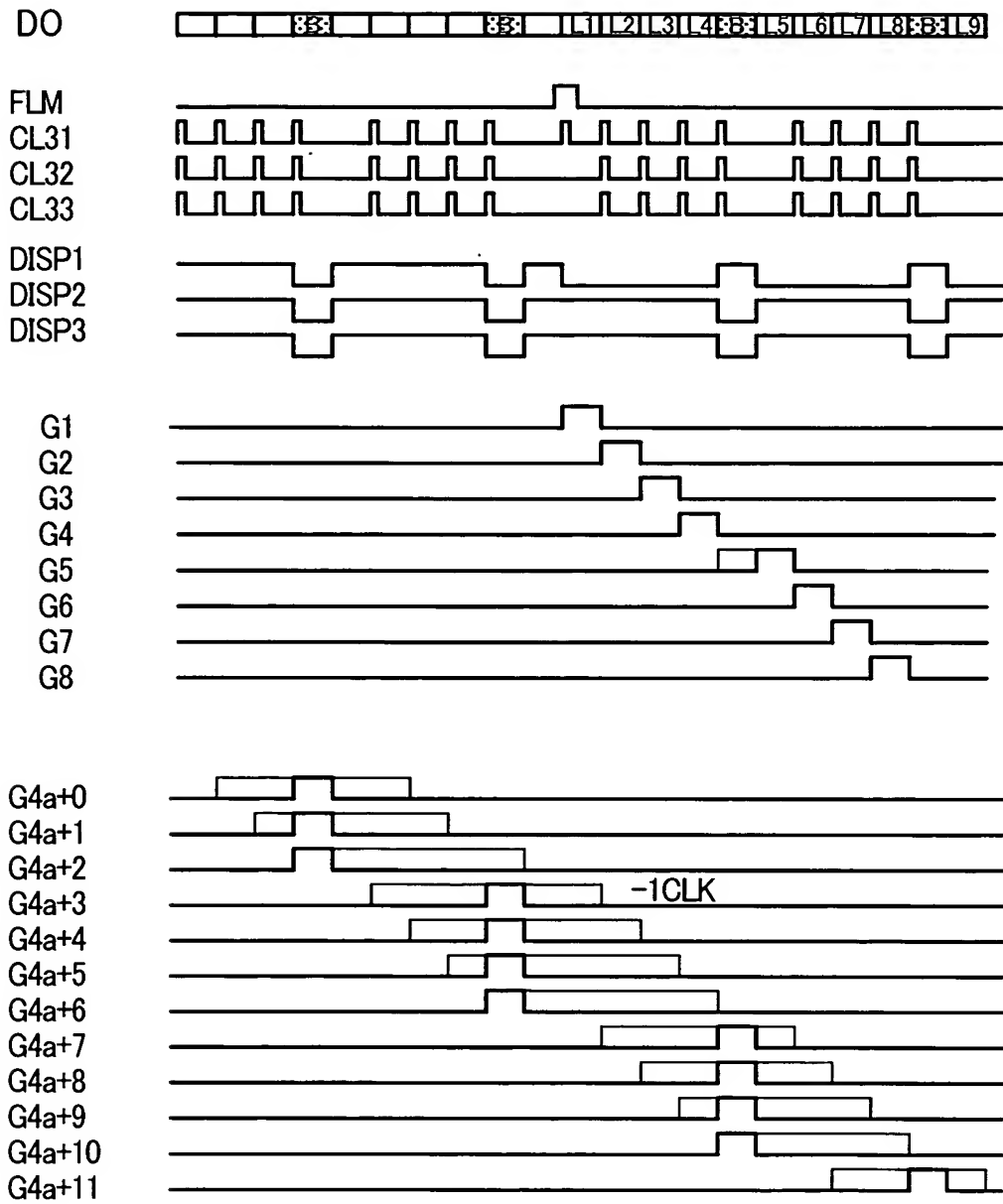
# FIG. 14

4n+0 F3 ② → F4 ④



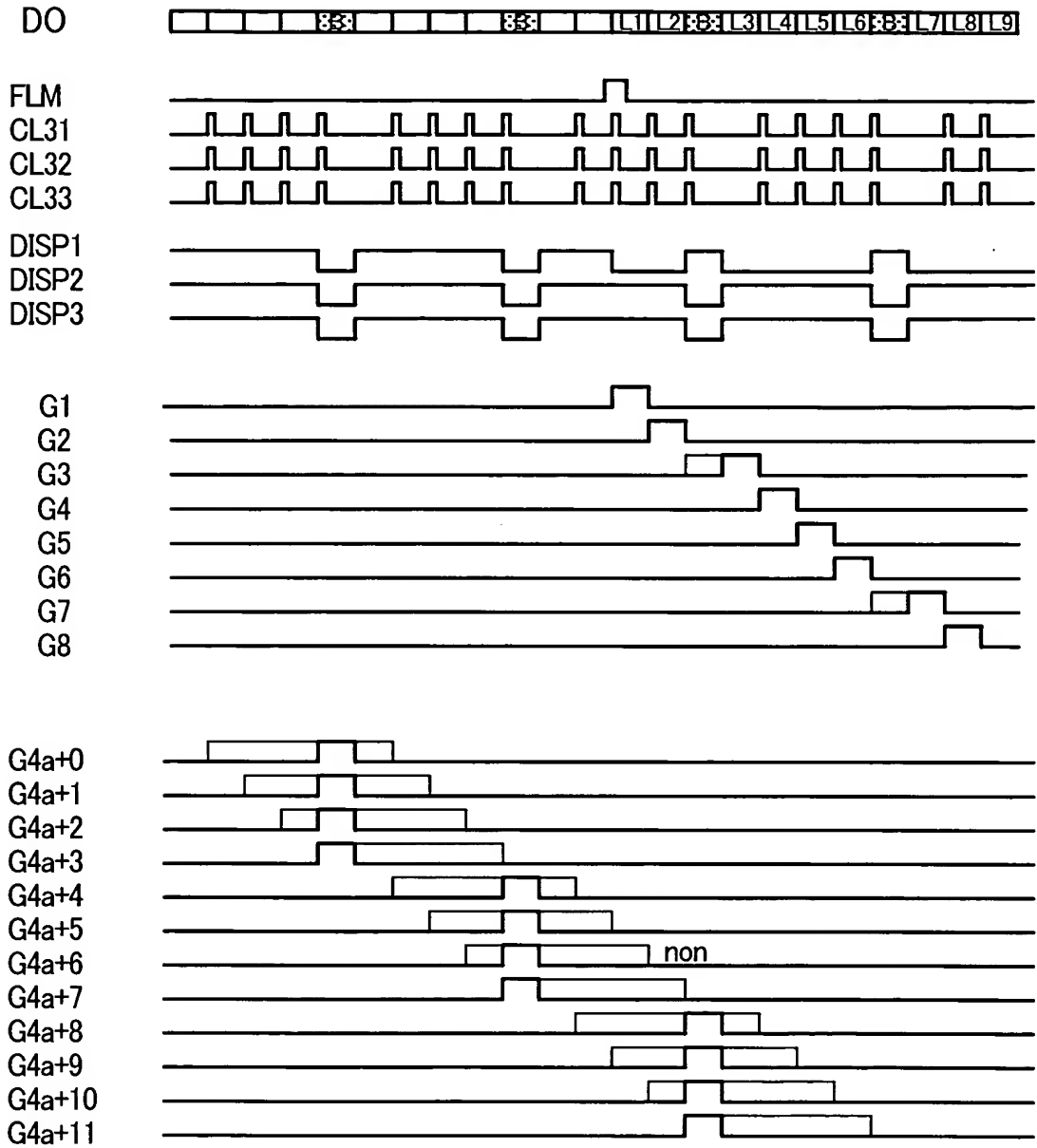
*FIG. 15*

4n+0 F4 ④ → F1 ①



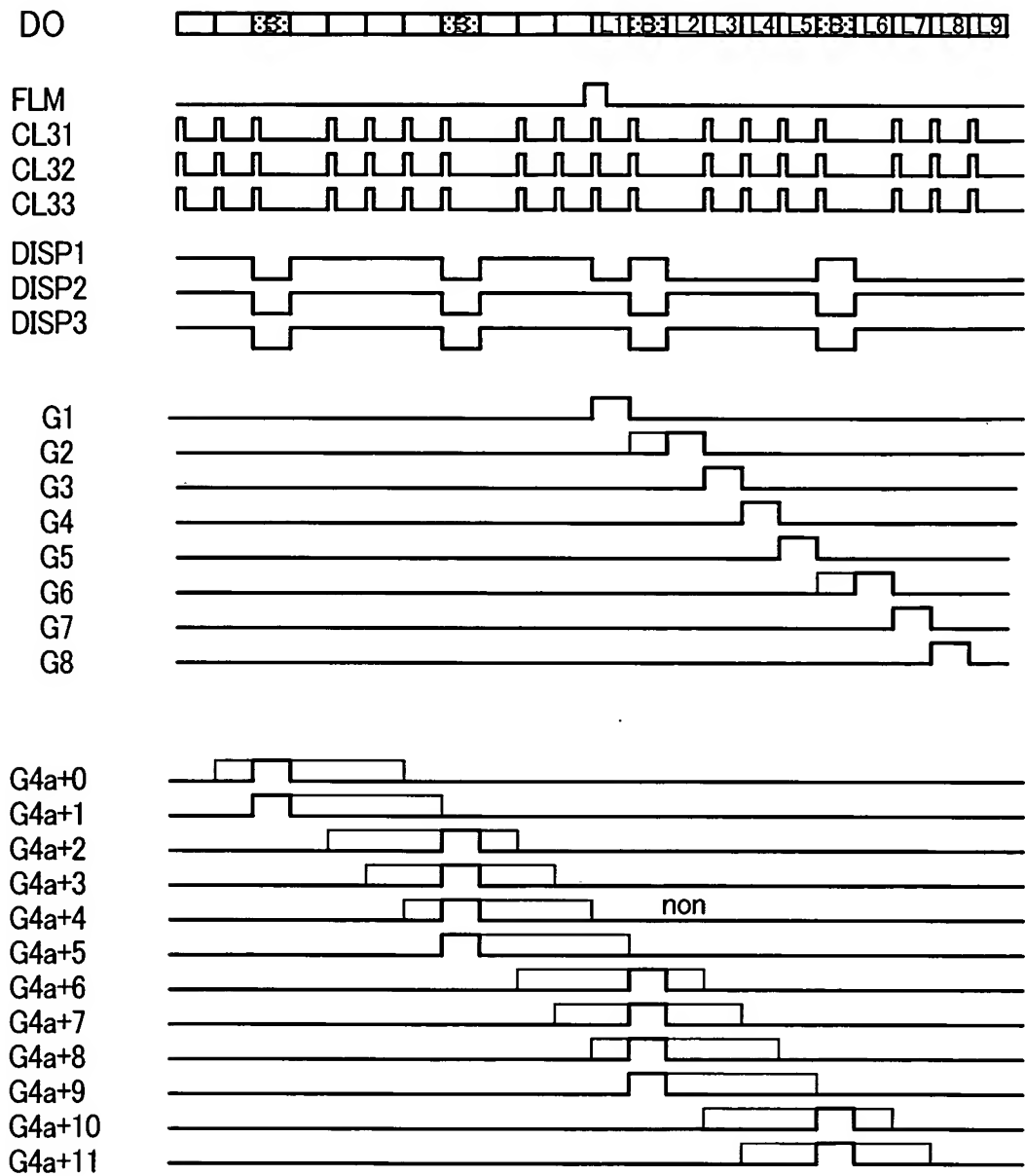
# *FIG. 16*

$4n+1$  F1 ①  $\rightarrow$  F2 ③



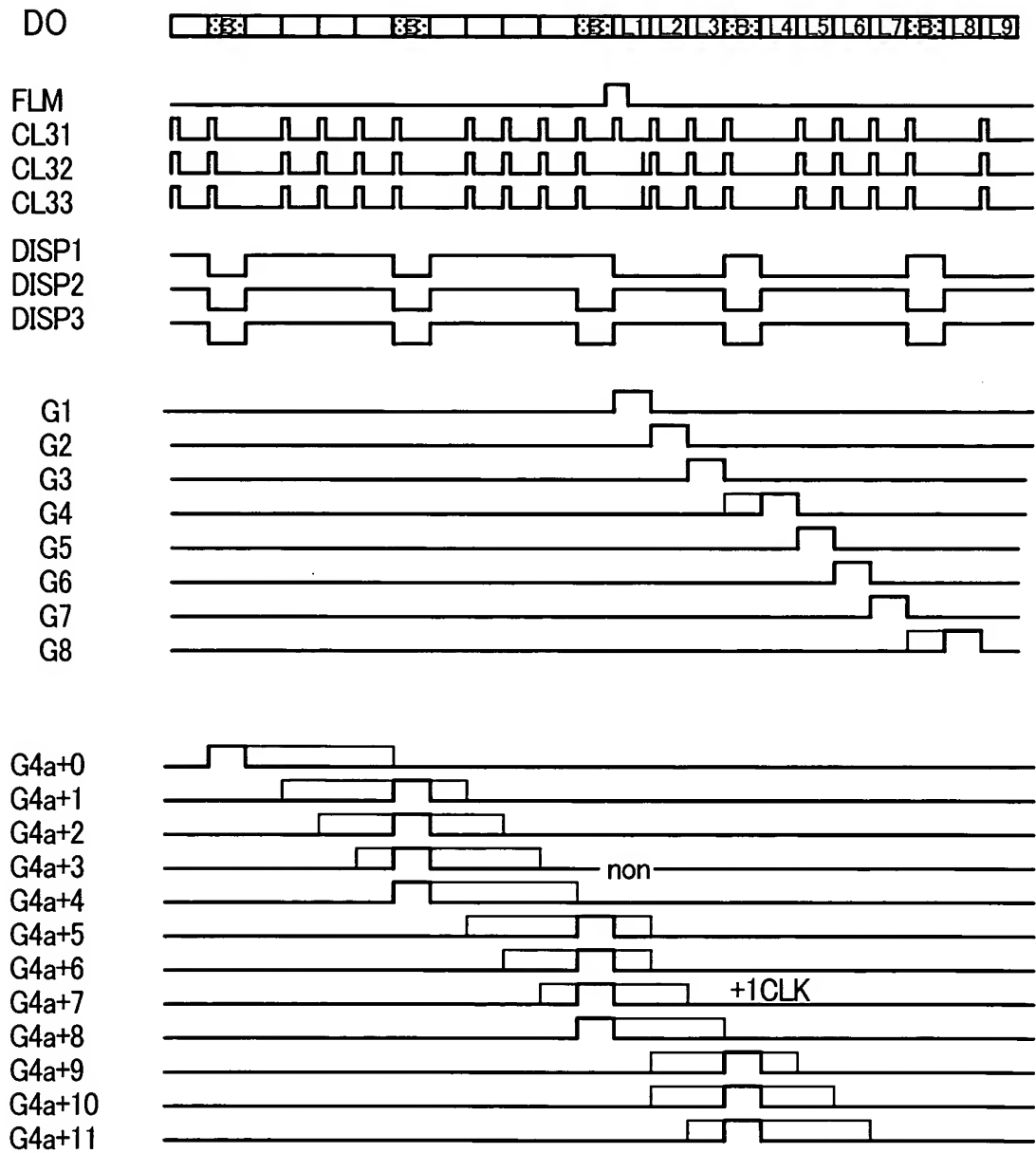
# FIG. 17

4n+1 F2 ③ → F3 ②



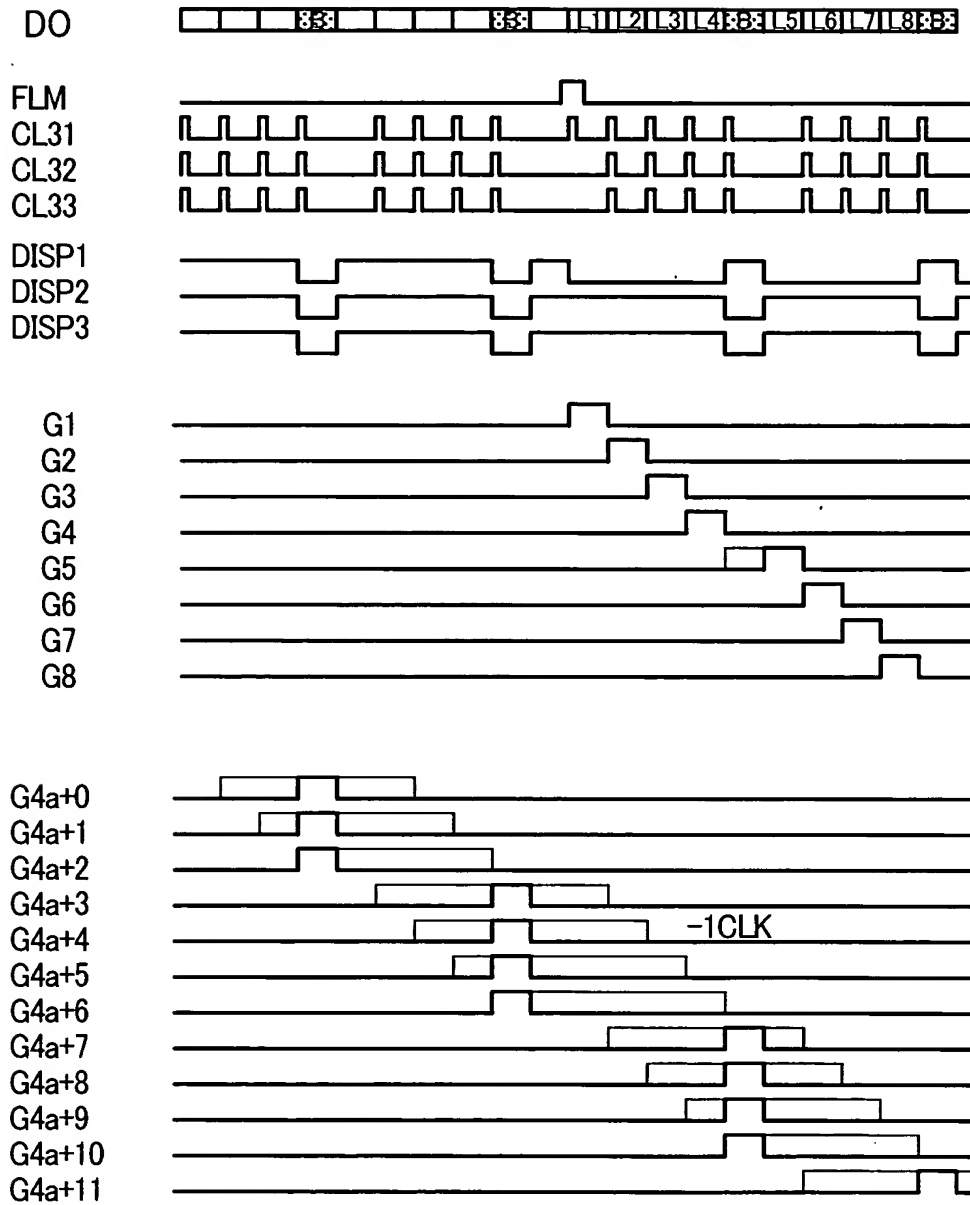
# FIG. 18

4n+1 F3 ② → F4 ④



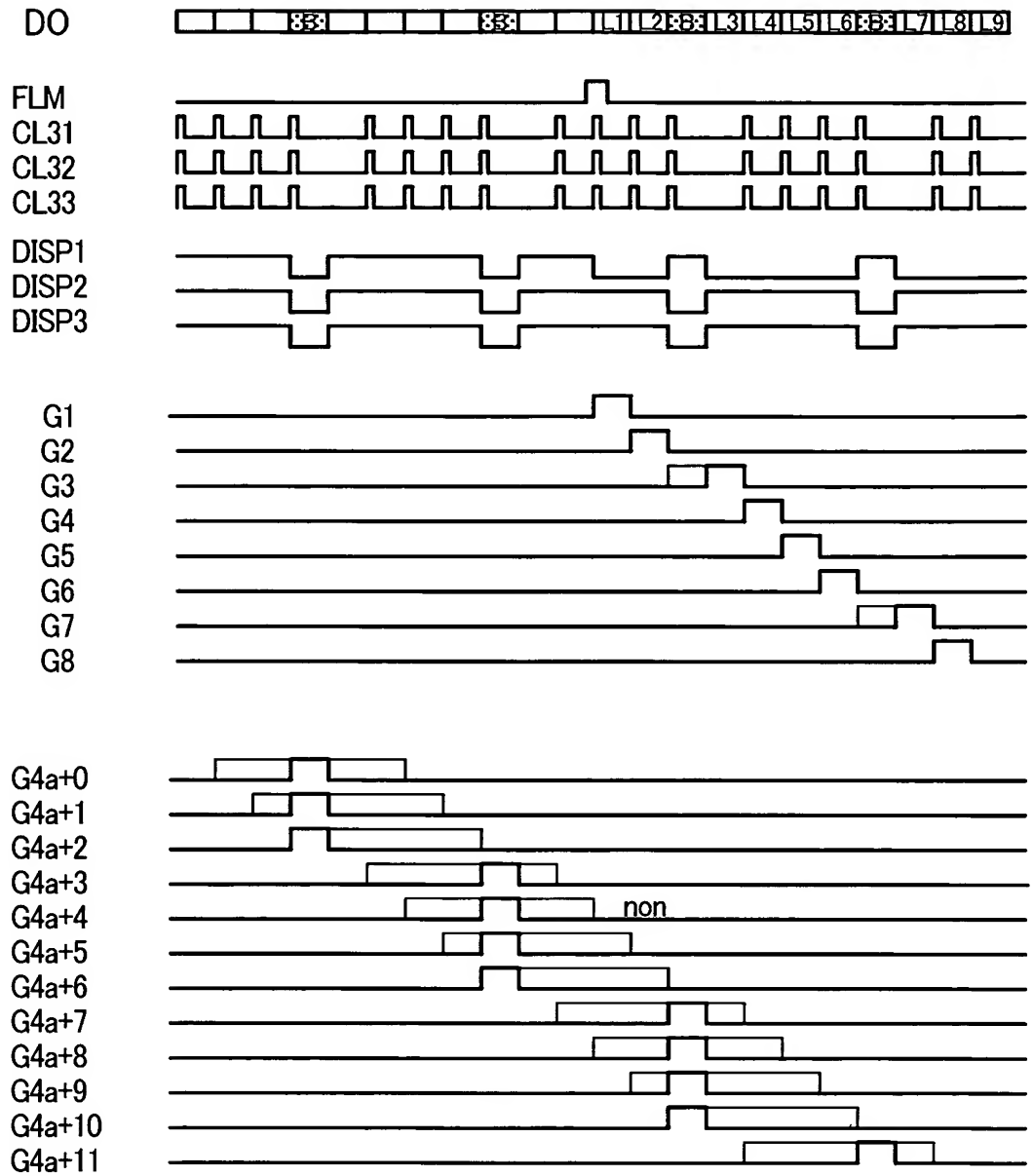
# *FIG. 19*

$4n+1$  F4 ④ → F1 ①



# FIG. 20

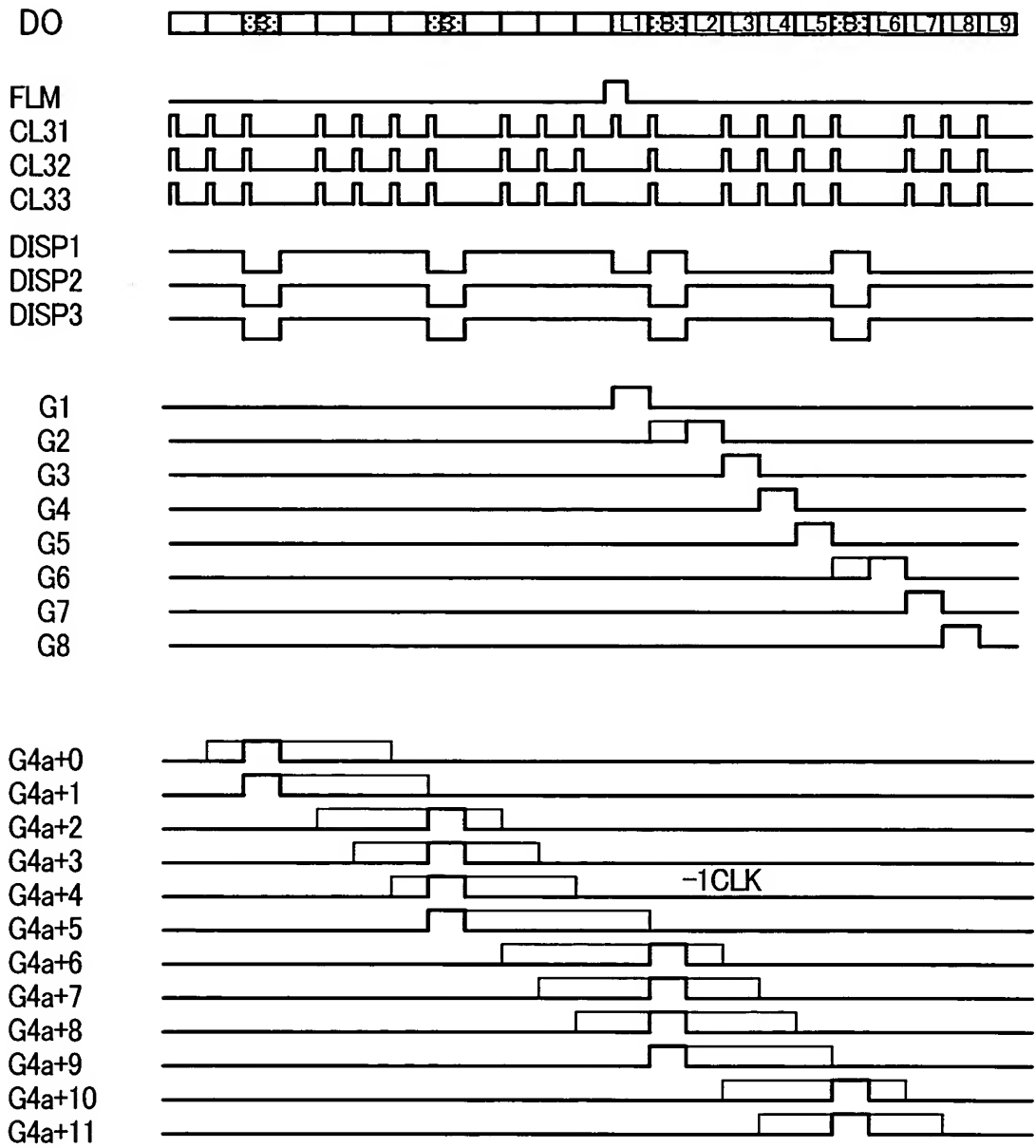
$4n+2$  F1 ① → F2 ③





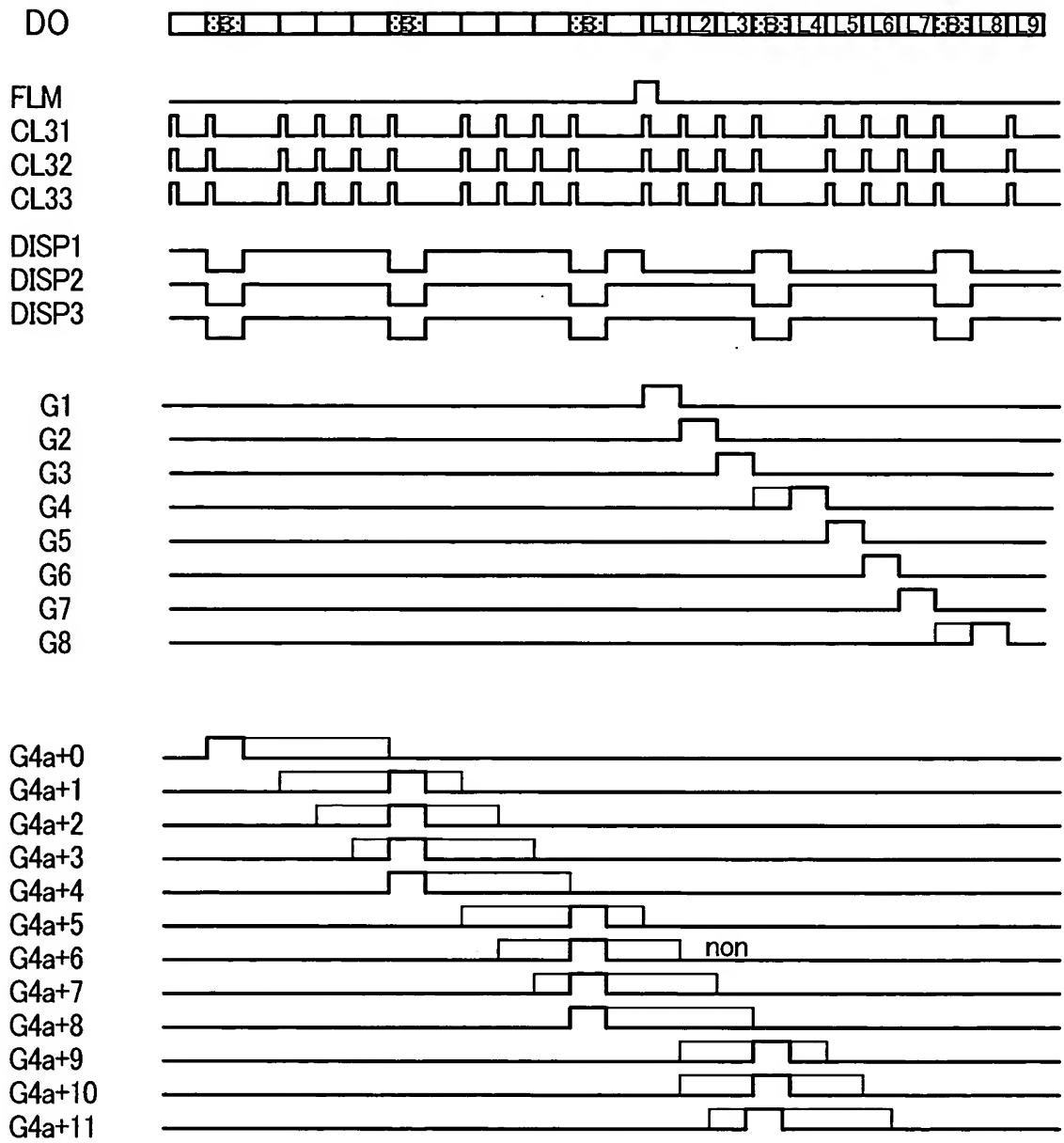
# FIG. 21

4n+2 F2 ③ → F3 ②



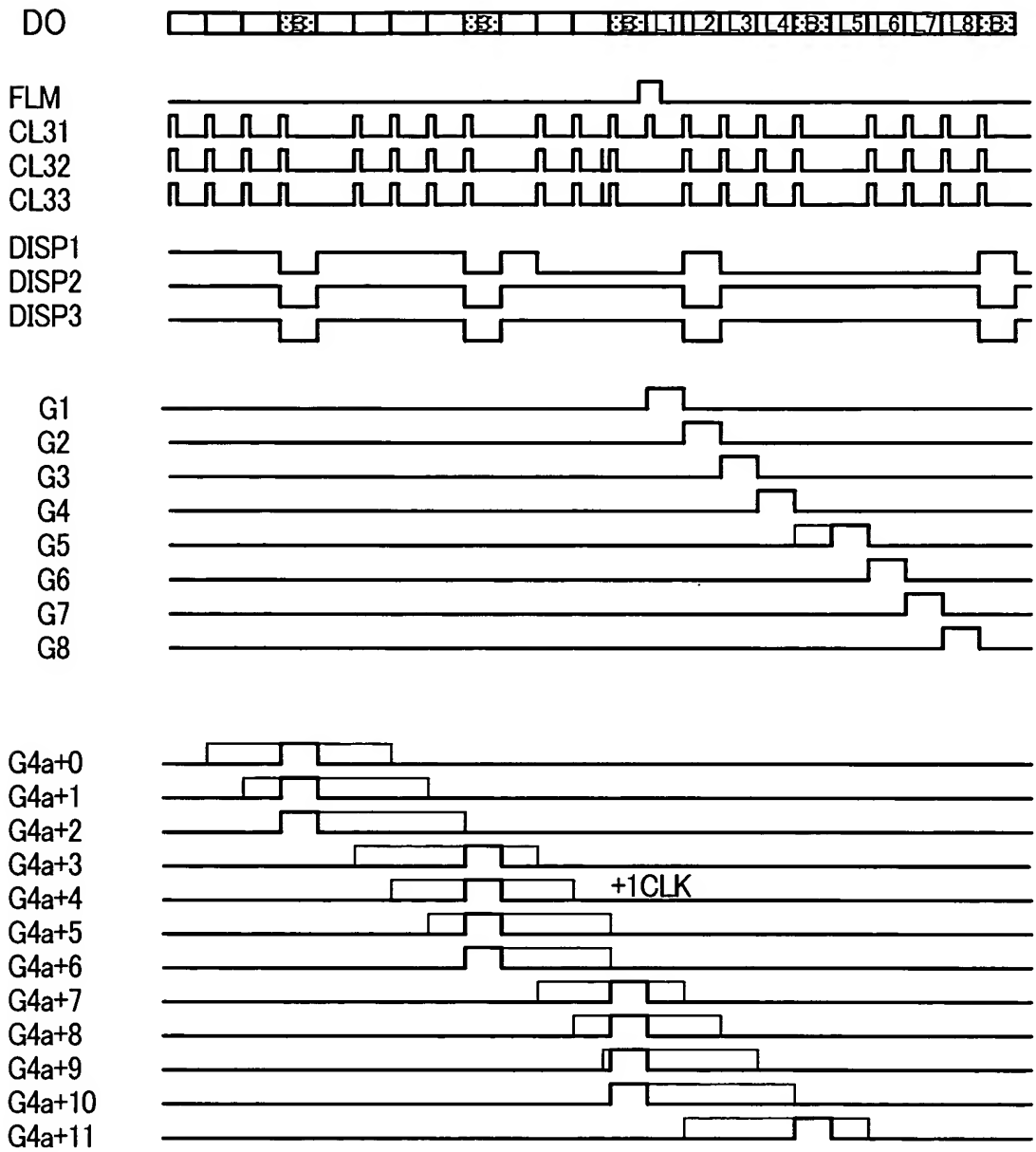
# FIG. 22

$4n+2$  F3 ②  $\rightarrow$  F4 ④



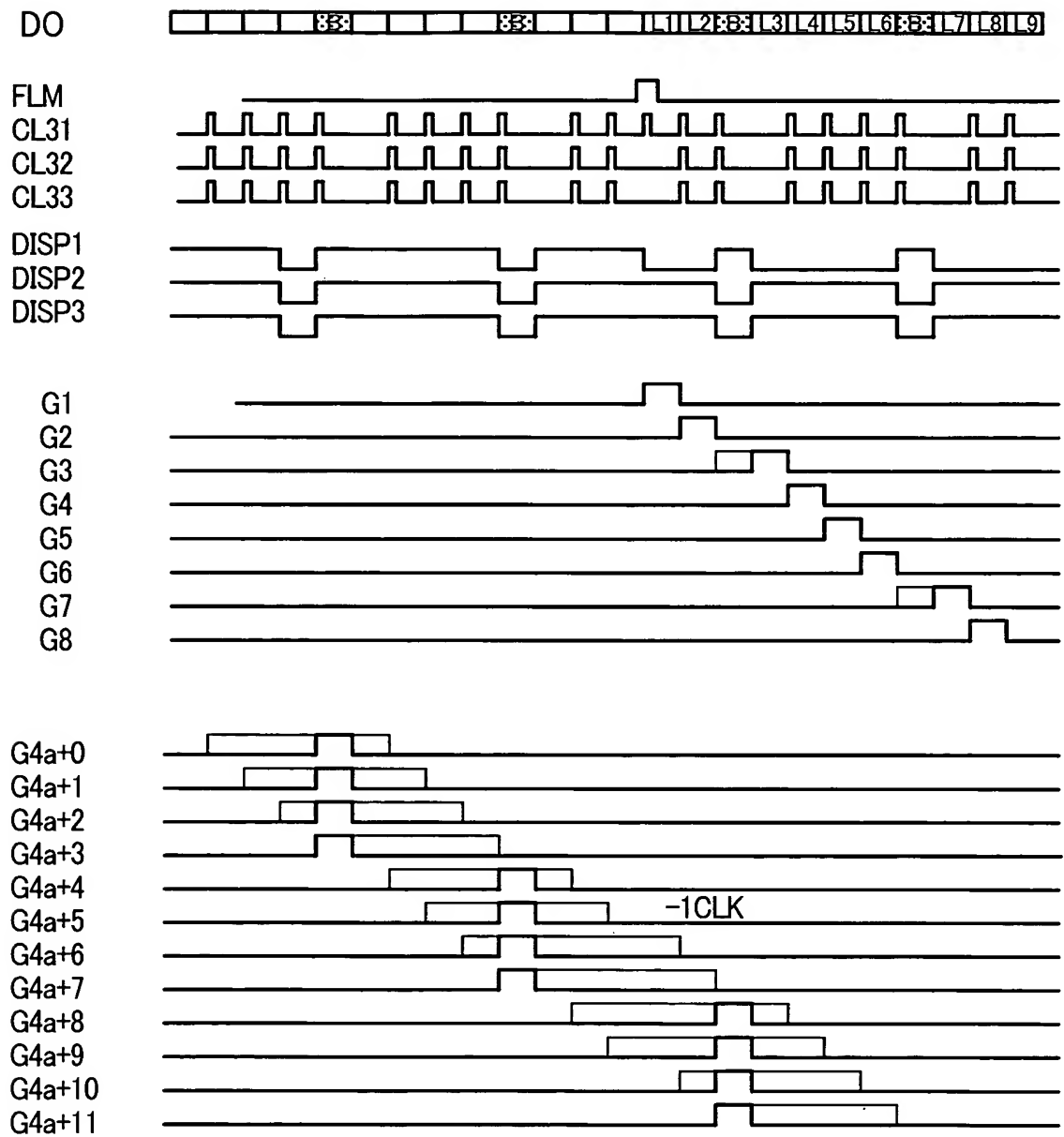
# FIG. 23

$4n+2$  F4 ④  $\rightarrow$  F1 ①



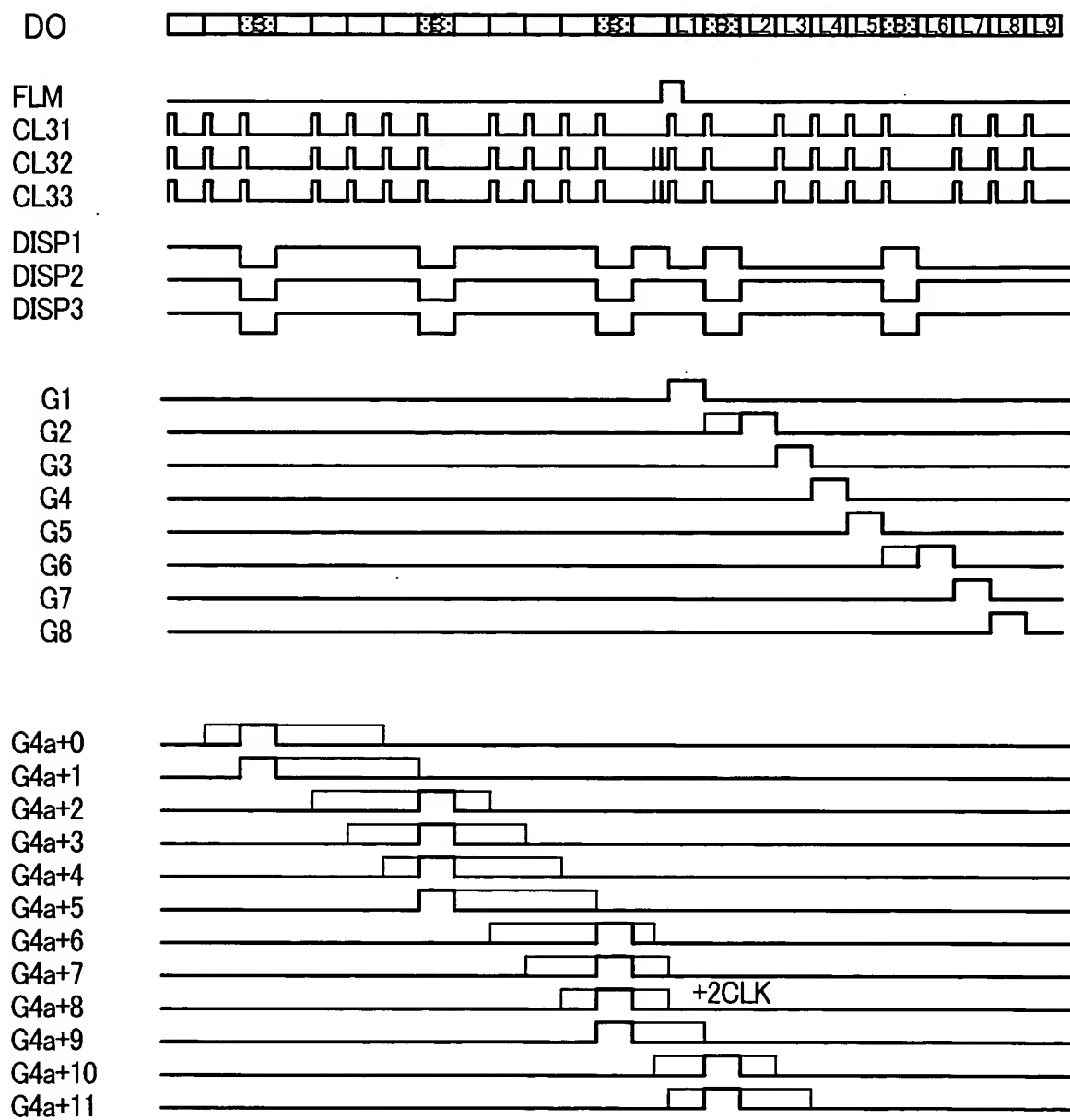
# FIG. 24

$4n+3$  F1 ①  $\rightarrow$  F2 ③



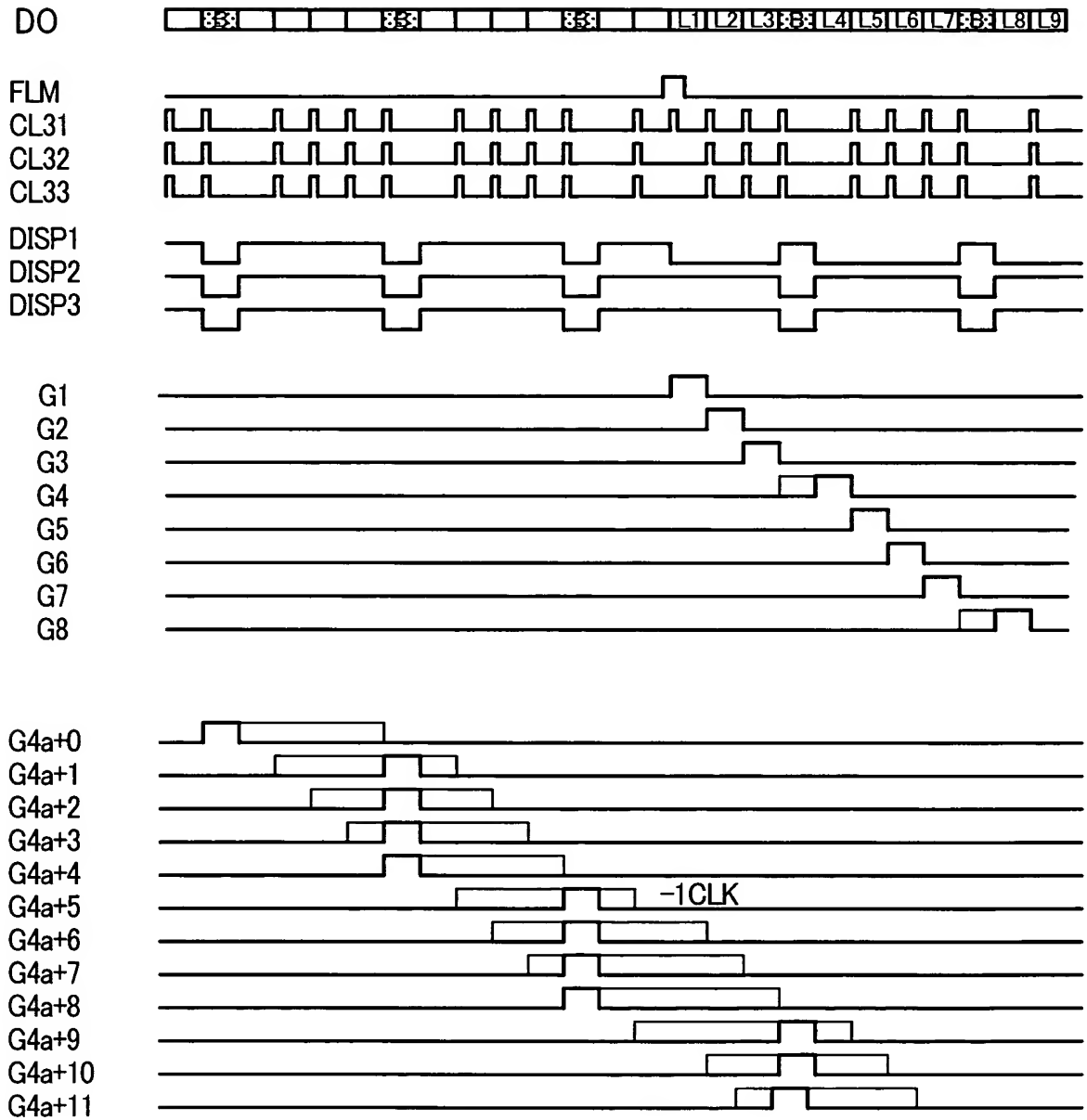
# FIG. 25

4n+3 F2 ③ → F3 ②



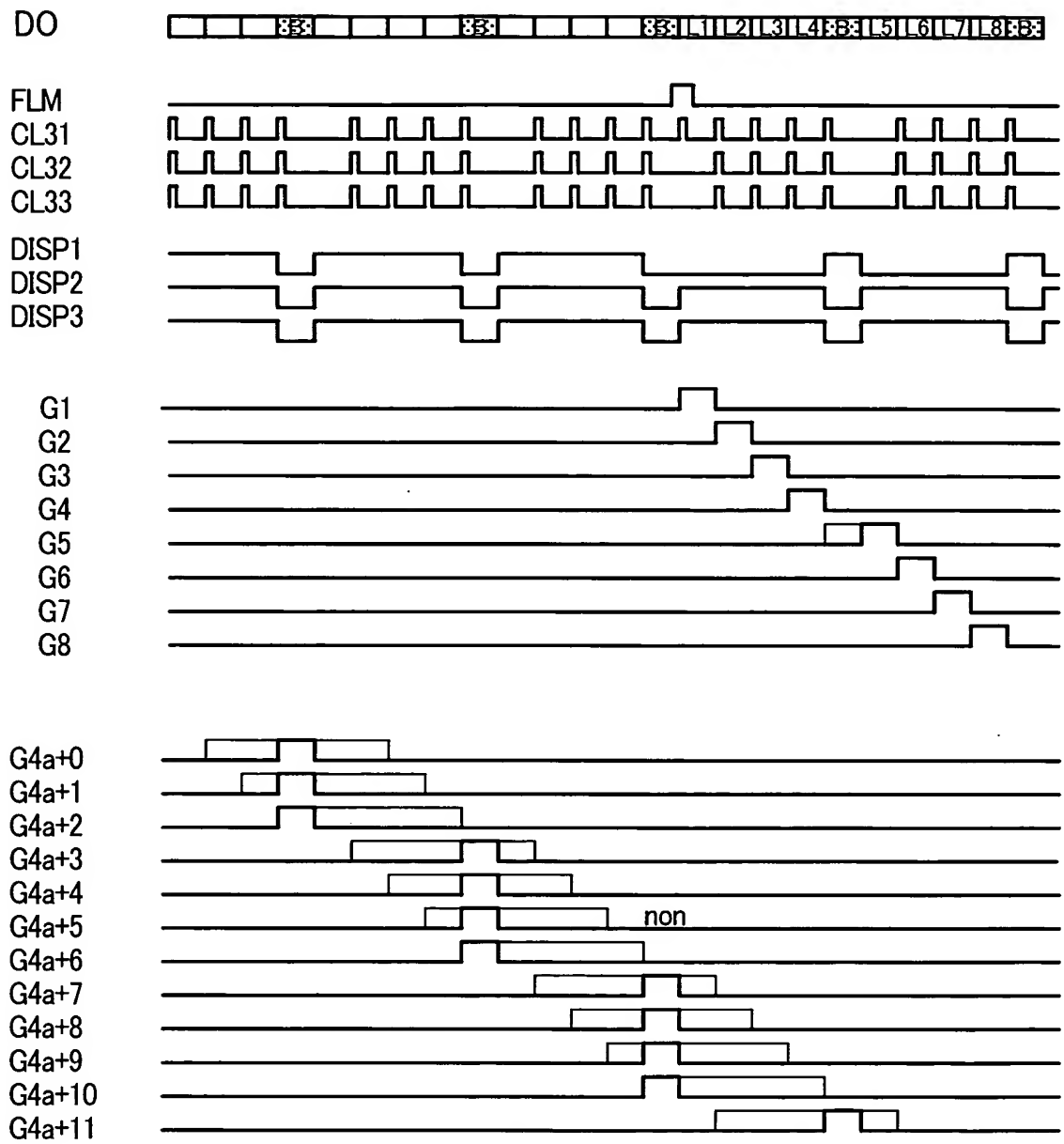
# FIG. 26

4n+3 F3 ② → F4 ④

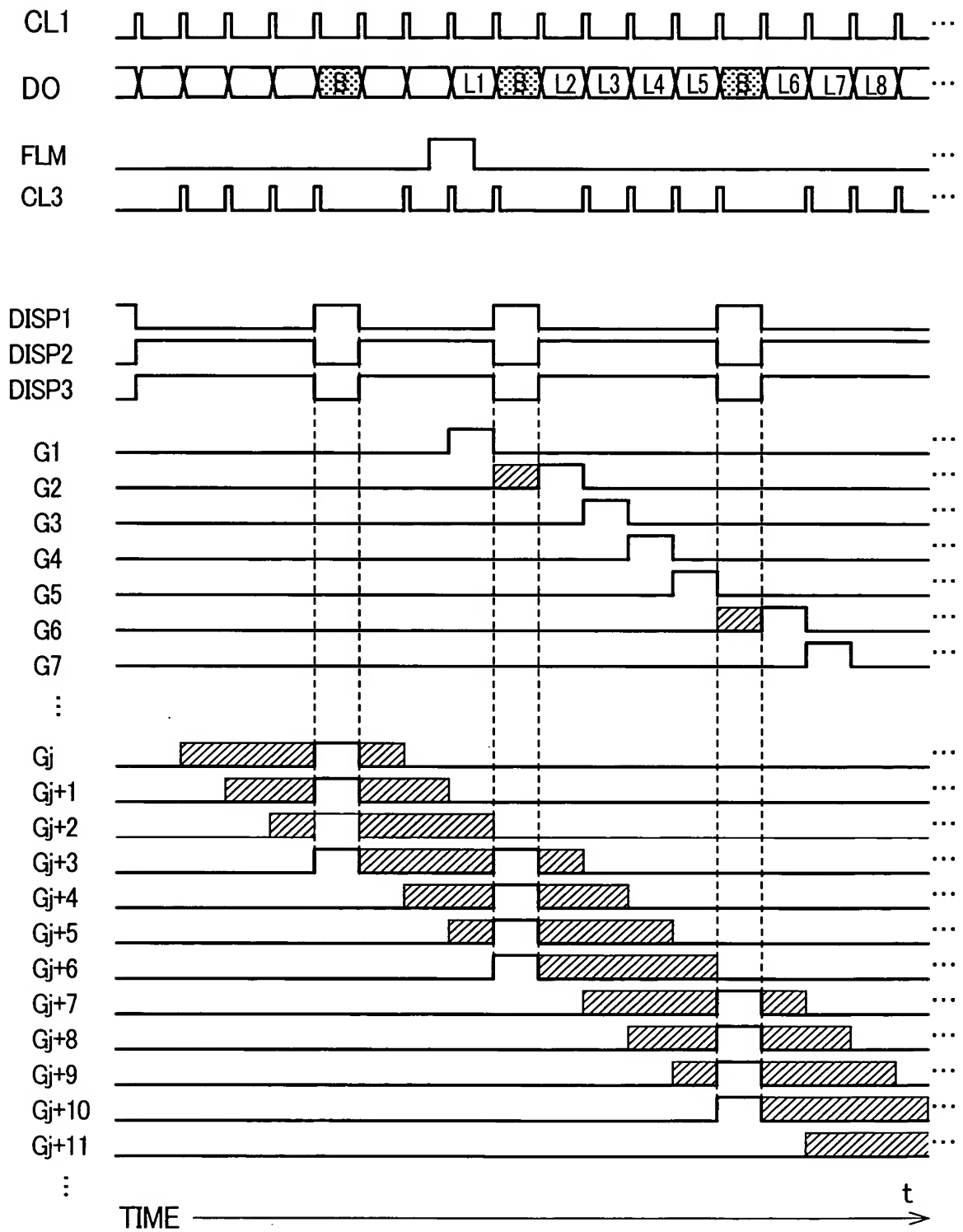


# FIG. 27

4n+3 F4 ④ → F1 ①

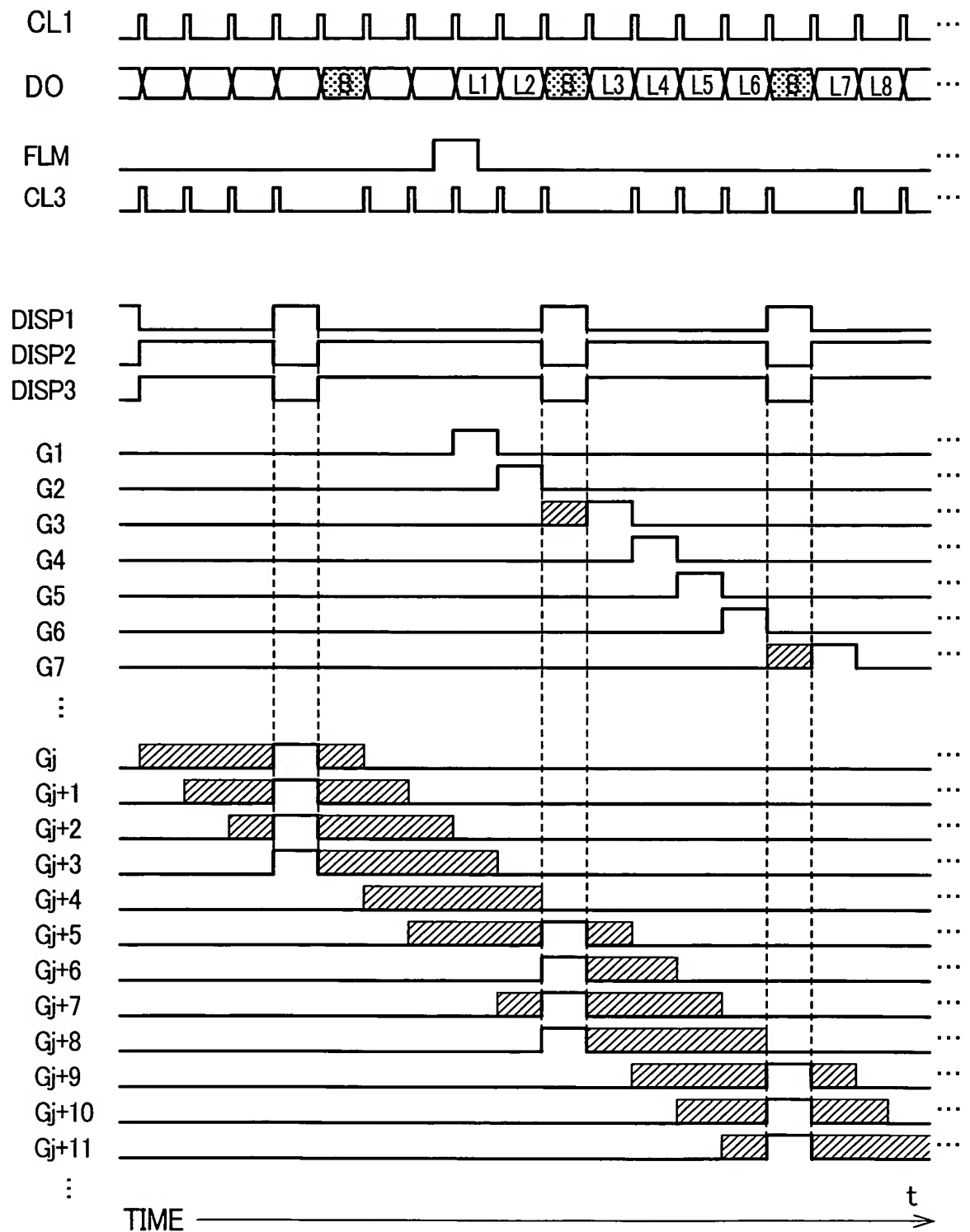


*FIG. 28*

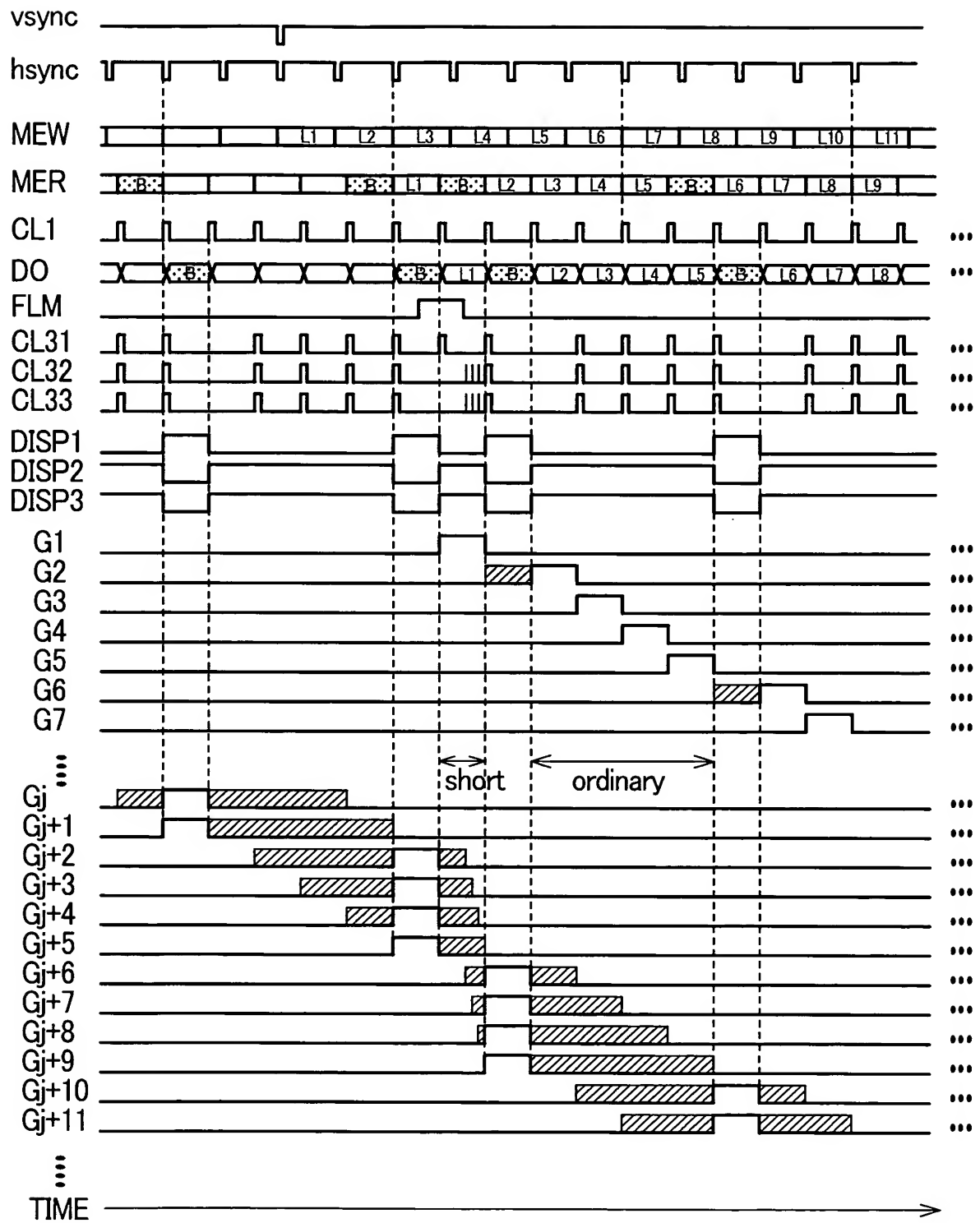




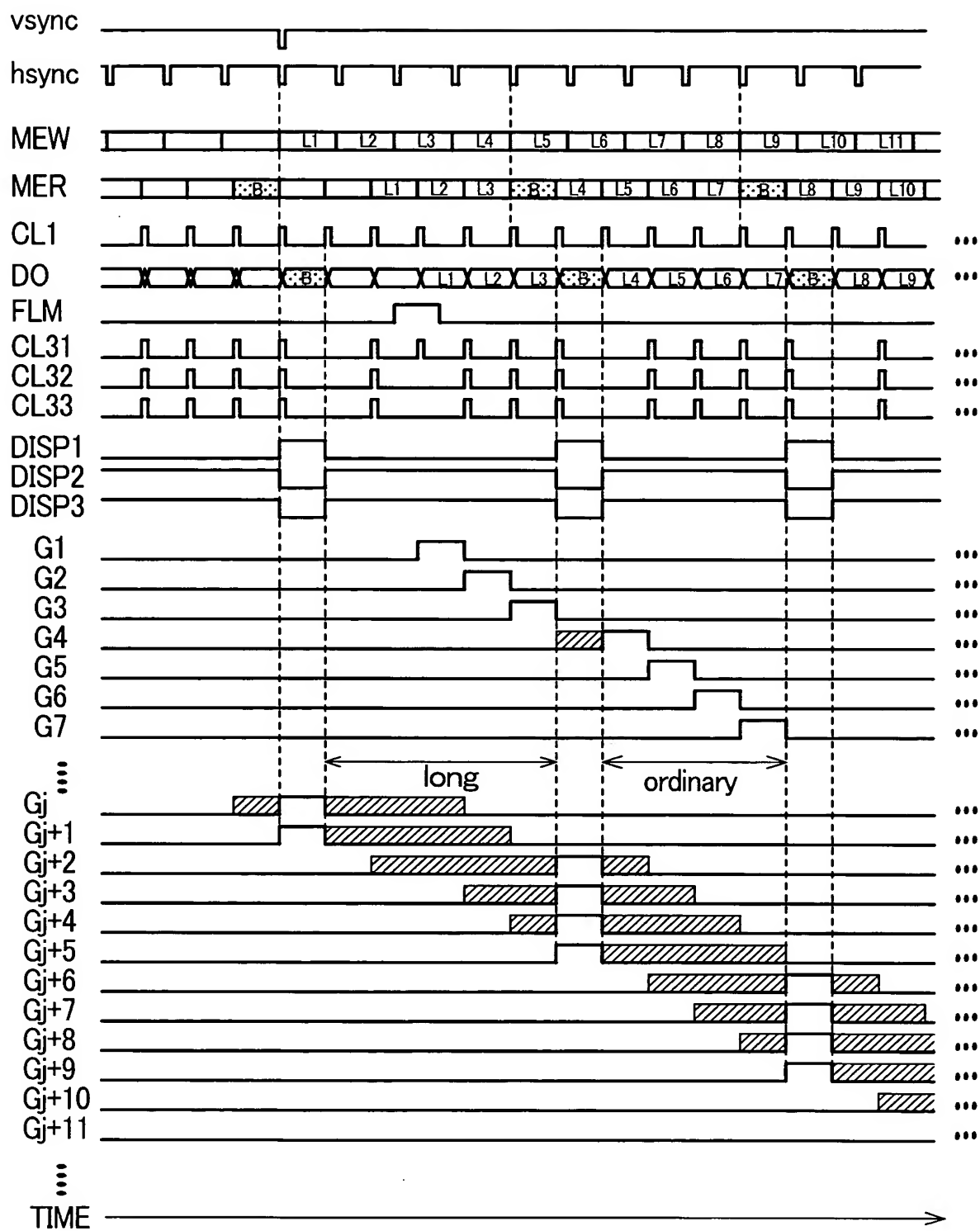
*FIG. 29*



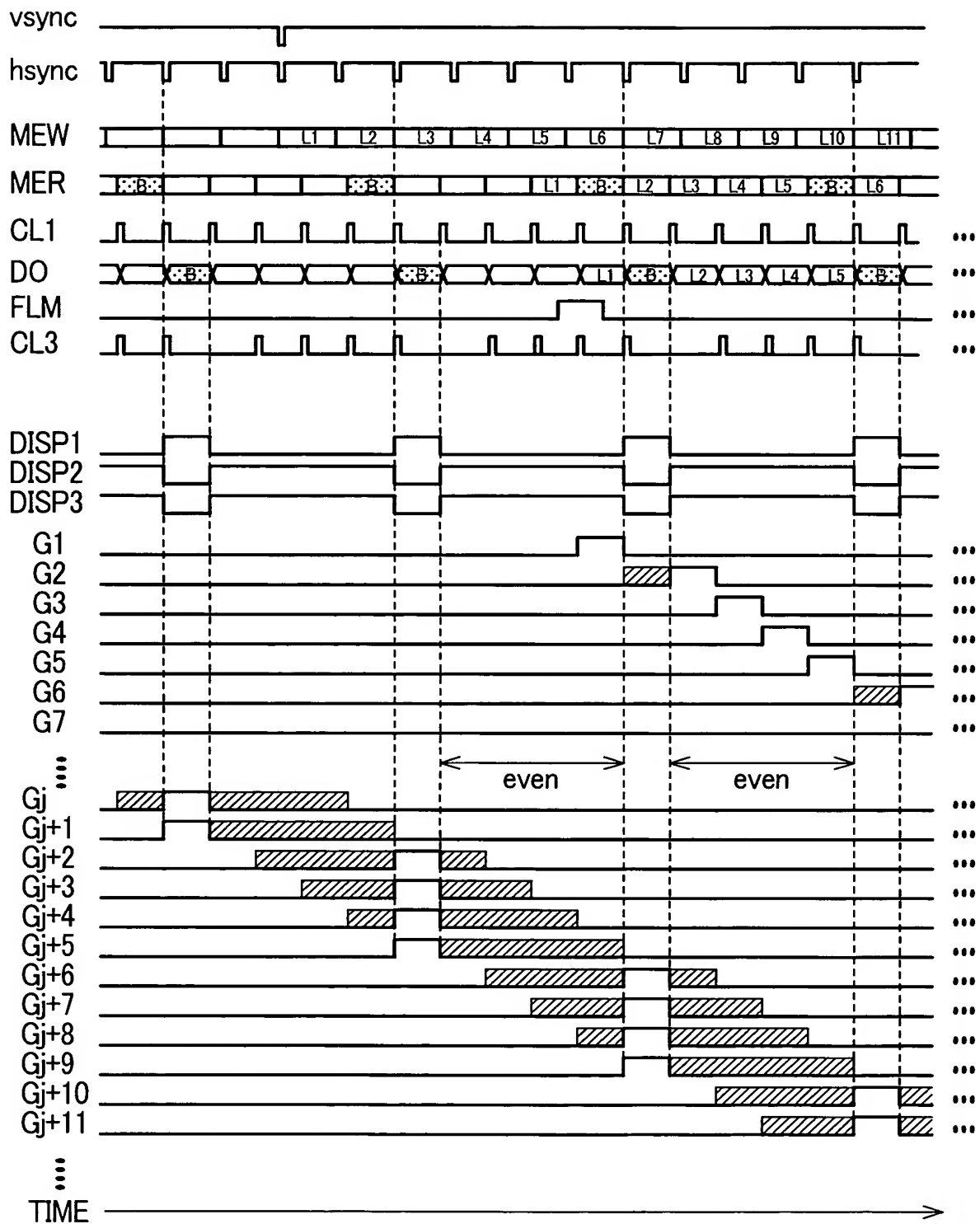
# FIG 30



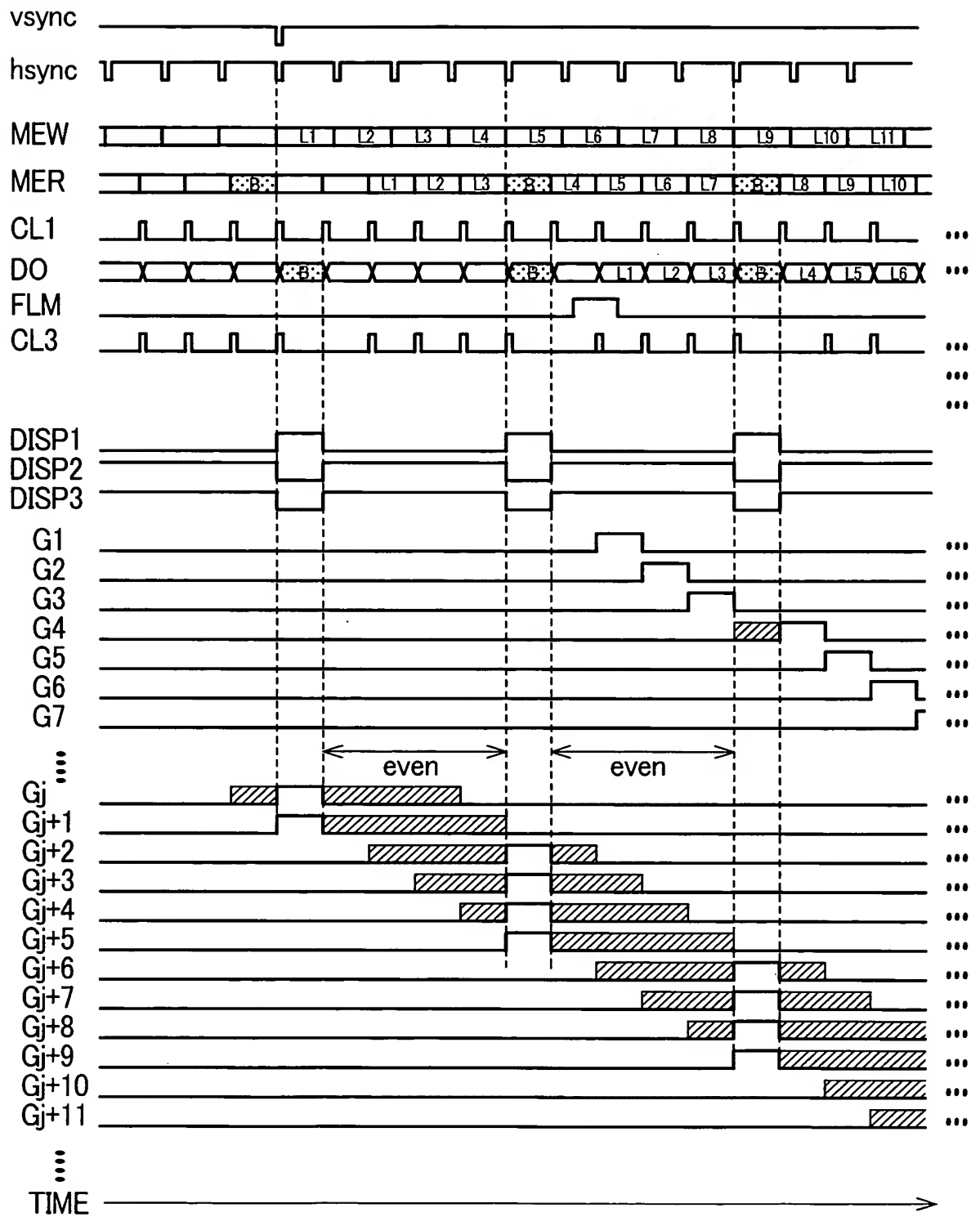
# FIG. 31



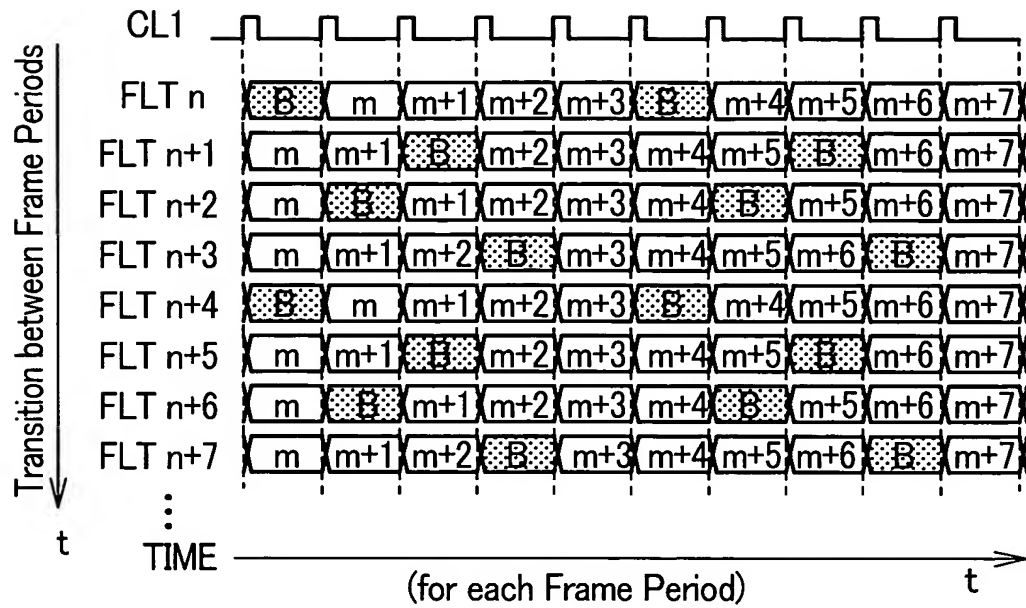
# FIG. 32



# FIG. 33



# FIG. 34



# FIG. 35

